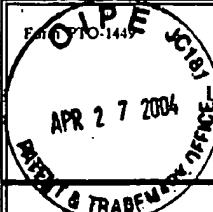


Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-1427		SERIAL NO. 09/559,903	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Zhiping Yin et al.			
				FILING DATE April 26, 2000		GROUP 2815	
U.S. PATENT DOCUMENTS							
*Examiner		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
Yes	AA	6,153,504	11-2000	Shields et al.			
	AB						
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	AE						
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	AH						
	AI						
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	AL						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation Yes No
Yes	AM	07201716 A	08-1995	Japan			X
Yes	AN	08046186 A	02-1996	Japan			X
Yes	AO	10-163083	06-1998	Japan			X
	AP						
	AQ						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
	AR						
	AS						
EXAMINER <i>J. E. Hunt</i>				DATE CONSIDERED <i>6/14/04</i>			
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LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Zhiping Yin et al.		FILING DATE April 26, 2000		
				GROUP 2815				
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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
YK	AA	4,971,655	11-1990	Stefano et al.				
YK	AB	6,498,084 B2	12-2002	Bergemont				
YK	AC	5,036,383	07-1991	Mori				
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	AH							
	AI							
	AJ							
	AK							
	AL							
FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
YK	AM	08-045926A	02-1996	Japan (abstract only)				
YK	AN	JP8051058	02-1996	Japan (abstract only)				
YK	AO	JP63316476	12-1988	Japan (abstract only)				
YK	AP	JP8078322	03-1996	Japan (abstract only)				
	AQ							
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
YK	A R		Stanley Wolf, SILICON PROCESSING FOR THE VLSI ERA, Vol. 3, Lattice Press 1995, page 635.					
	A S							
EXAMINER				DATE CONSIDERED				
YK				6/14/04				
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